

IN THE CLAIMS:

1. (Currently Amended) A phase locked loop system for integration onto an integrated circuit comprising a charge pump (12) arranged to output a first current over a first charge pump path while a second current is output over a second charge pump path; and a phase locked loop filter (13) ~~having~~ characterised in that it comprises only a first capacitor (C2) electrically coupled between ~~to~~ the first charge pump path and the second charge pump path; and a parallel resistor/capacitor circuit (23) electrically coupled to the second charge pump path with the resistor/capacitor circuit (23) having a second capacitor (C1) and a first resistive element; wherein the first capacitor (C2) and second capacitor (C1) are connected in series to allow a voltage associated with the first capacitor (C2) and a voltage associated with the parallel resistor/capacitor circuit (23) to be added together, and an extra pole having a second resistive element and a shunt capacitor coupled, at one end to the second resistive element and at the other end to a reference voltage.

2. (Currently Amended) A phase locked loop system according to claim 1, wherein the current flow in the second path is greater than the current flow in the first path to allow a decrease in the capacitance of the phase locked loop filter (13).

3. (Cancelled.)

4. (Currently Amended) A phase locked loop system according to ~~any preceding~~ claim 1, wherein the added voltage is arranged to control a voltage controlled oscillator (13).

5. (Currently Amended) An electronic device incorporating a phase locked loop system according to ~~any preceding~~ claim 1.

6. (Currently Amended) A radiotelephone incorporating a phase locked loop system according to ~~any preceding~~ claim 1.